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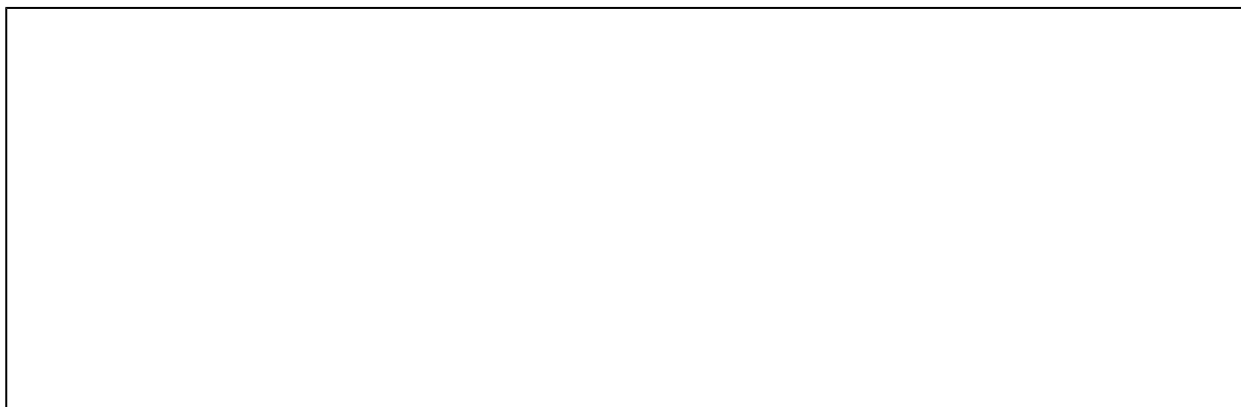
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8	CMOS GHz 6bit ADC	10	2014	10.0000		A
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1	Low capacitance and highly reliable blind through-silicon vias (TSVs) with vacuum assisted spin coating of polyimide dielectric liners	Yan, Yangyang ; XI ONG Miao; LI U Bin ; Di ng, Yi ngtao CHE N Zhi Ming	2016		SCIENCE CHINA-TECHNOLOGICAL SCIENCES	1.4
2	Modeling and Analysis of Vertical Noise Coupling in TSV-Based 3D Mixed-Signal Integration	Shiwei Wang; Yi ngtao Di ng; Zhi ming Chen; Huanyu He; Ji an-Qiang Lu	2016		Microelectronic Engineering	1.3
3	Wideband Capacitance Evaluation of Silicon-Insulator-Silicon Through-Silicon-Vias for 3D Integration Applications	Xi nghua Wang; Miao Xi ong; Zhi ming Chen; Bohao Li ; Yangyang Yan; Di ng, Yi ngtao	2016		IEEE ELECTRON DEVICE LETTERS	2.5
4	Highly conformal polyimide liner deposition in high-aspect-ratio through-silicon vias (TSVs)	Yi ngtao Di ng; Yangyang Yan; Miao Xi ong; Shiwei Wang ; Qianwen Chen; Zhi ming Chen	2016		Micro & Nano Letters	0.7

5	Study of Vacuum Assisted Spin Coating of Polymer Liner for High-Aspect-Ratio Through-Silicon-Via Applications	sY. Yan ; Ding, Yingtao	2016	IEEE TRANSACTIONS ON COMPONENTS		1.2
6	Innovative polyimide liner deposition method for high-aspect-ratio and high-density through-siliconvias (TSVs)	Ding, Yingtao; Miao Xiong; Yangyang Yan; Shiwei Wang; Qianwen Chen; Weijiang Wang	2016	Microelectronic Engineering		1.3
7	A Truly Balanced Q-Band CMOS Frequency Doubler Based on Hybrid Quadrature Coupler	?Jiayue Wang?; ??Zhiming Chen?; ??Qiang An?; ??Xinghua Wang	2016	IEEE Microwave and Wireless Components Letters		1.6
8	An analytical model for capacitance of silicon-insulator-silicon through-silicon-vias	Bohao Li ; An'an Li ; Miao Xiong ; Ji anxun Yang ; Zhiming Chen ; Yingtao Ding	2016	Miao Xiong ; Ji anxun Yang ; Zhiming Chen ; Yingtao Ding		0.8
9	DOA Estimation with Enhanced DOFs by Exploiting Cyclostationarity	Li u, Ji anyan Lu, Yilong Zhang, Yanwei Wang, Weijiang	2016	IEEE TRANSACTIONS ON SIGNAL PROCESSING		2.6
10	An accurate power and temperature simulation framework for Network-on-Chip	Ji anxun Yang?; ??Shan Cao	2016	?Integrated Circuits and Microsystems (ICM)		



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1		ZL201310280690 .6		2016			
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5	XXX XXX	ZL 20131800393 6.6		2015			
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28	RF nems				2015		10.0
29	SOC				2015		300.0
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31	VGA/DVI				2015		54.4

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34					2015		72.85
35	XX				2015		400.0
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37	XX				2015		10.0
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